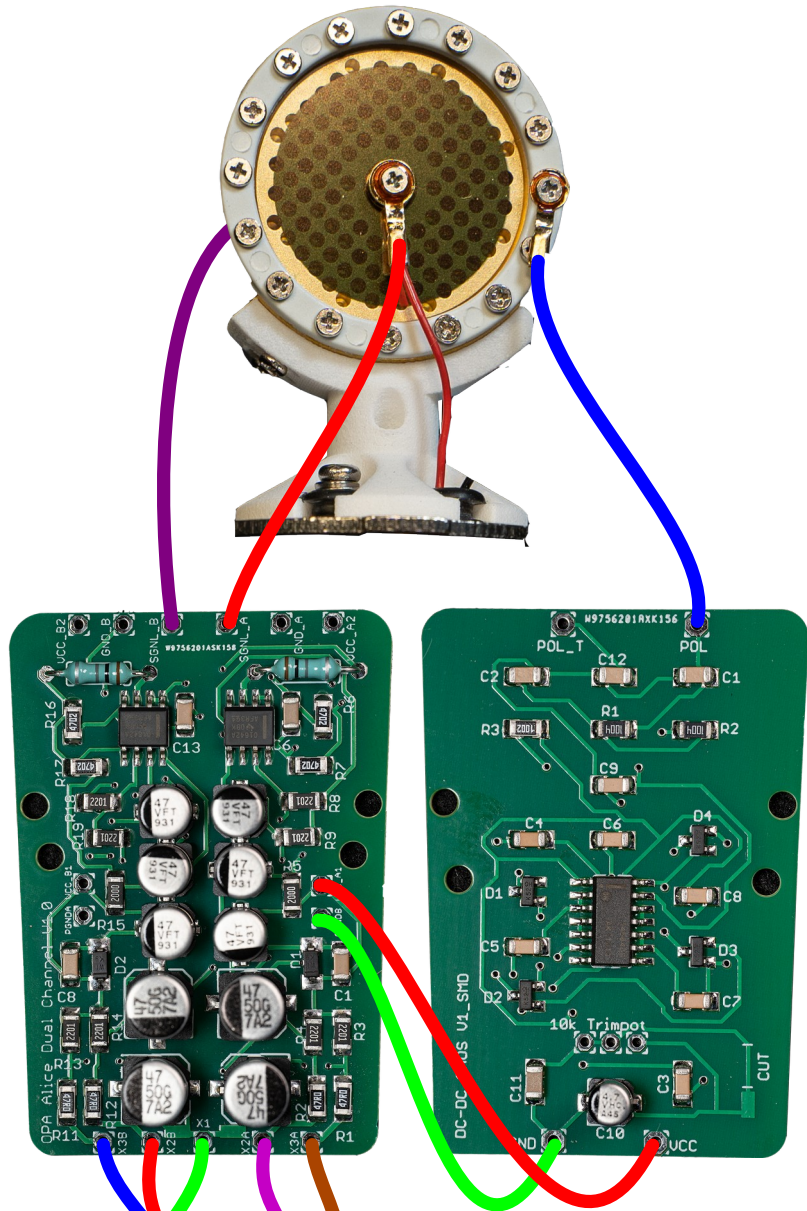


Here are the connections for a dual output condenser capsule.

The center terminal of the front capsule face goes to “SGNL_A”.

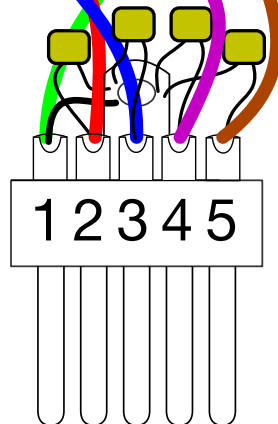
The terminal of the rear facing face goes to “SGNL_B”.

The edge or body goes to the polarizing voltage “POL”.



XLR Pin 1, to XLR1, XLR Pin 2 and 3 to XLR2a, XLR3a, and XLR Pin 4 and 5 to XLR2b, XLR3b, on the PCB.

Connect four 22nF capacitors between Pins 2, 3, 4, and 5 of the XLR and Ground. This should be as close the XLR jack as possible for best EMI/RF suppression. Connect Pin 1 to Ground Lug.



The VCC and GND connections of the CMOS voltage multiplier board connect to VCC_A1 and DGND_B on the Alice Opamp board.